

Steering Committee

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IEEE CASS VSA-TC Liaison

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Technical programme Committee

Track Chairs:

- Analog: t.b.d.
- Digital: t.b.d.
- SoC: t.b.d.

Members:

- Orazio Aiello, National Univ. of Singapore (SG)
- Janne Aikio, Univ. of Oulu (FI)
- Paul Annus, Taltech (EE)
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- Marco Balboni, University of Ferrara (IT)
- Elmars Bekeris, Riga Technical University (LV)
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- Claudio Brunelli, Nokia (FI)
- Erik Bruun, DTU (DK)
- Luigi Carro, UFRGS (BR)
- Mario Casu, Politecnico di Torino (IT)
- Kun-Chih (Jimmy) Chen, National Sun Yat-sen University (TW)
- Yong Chen (Nick), Univ. of Macau (MO)
- Patricia Derier, National Instruments (US)
- William Fornaciari, Politecnico di Milano (IT)
- Alberto Garcia Ortiz, Univ. of Bremen (DE)
- Diana Goehringer, TU Dresden (DE)
- Gunnar Gudnason, Oticon (DK)
- Xinfei Guo, Mellanox Technologies (US)



Call for Papers

2024 IEEE NorCAS Conference

IEEE Nordic Circuits and Systems Conference
October 2024 Lund, Sweden



General Scope of the Conference

The IEEE NorCAS conference is the main circuits and systems event of the Nordic countries representing both academia as well as electronics industry. The IEEE NorCAS conference is a merge of the well-established conferences NORCHIP and SoC. The annual IEEE CAS co-sponsored conference covers all fields of electronics design, spanning from complex digital systems to advanced analog and mixed-mode circuits. The wide scope of the conference promotes cross-field collaboration not only between academics but also with industry. Papers of high scientific and technical quality are presented together with selected invited speakers and pre-conference tutorial sessions.

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Technical programme

The 2024 IEEE NorCAS conference will take place on 2 -30 October 2024. The Steering Committee invites people from academia and industry to contribute papers. Contributions for the 2024 conference in Aalborg shall be in the following areas:

ANALOG

- Biomedical circuits and systems
- Sensor interface circuits, data-converters
- Wireless circuits and systems
- Mixed mode designs, high frequency circuits, design for testability

DIGITAL

- Microelectronics/nanoelectronics
- Embedded systems, FPGA and prototyping
- Sensors, Imagers, MEMS devices
- Measurement, modeling, and simulation of hardware
- CAE, design tools, VLSI design methodology
- Machine learning, neural networks
- Packaging and interconnect
- System-level design flow and methodology

SoC

- Network-on-Chip
- Hardware/software co-design and reuse
- Multicore and manycore platforms
- Reconfigurable architectures
- SoC verification and testing
- Processor and memory design for SoC
- Runtime environments, emulation and simulation

Special Sessions

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Submission of Contributions

Papers will be selected by the Steering Committee on the basis of a full-length paper (4-6 A4 pages) based on a peer review performed by the Technical Programme Committee. There may be a 7th page with references only.

Papers may be selected for either lecture or poster presentation. Equal quality measures will be applied to posters and lectures. All accepted papers will be published in the conference proceedings and submitted for inclusion in IEEE Xplore.

Dates and Deadlines

Special session proposals: June 15, 2024

Paper submission deadline: August 15, 2024

Notification of acceptance: September 20, 2024

Final camera-ready papers: October 4, 2024

The proceedings will be composed from camera ready pdf files supplied by authors. The paper must follow the IEEE 2-column standard as described in the paper submission guidelines in the author info section at events.tuni.fi/norcass2023. Instructions on special session proposals are found on the web page, too.

The submission site is at www.conftool.pro/norcass2023

Top papers will be invited to be extended and submitted to a special section planned for IEEE Transactions on Very Large Scale (VLSI) Systems - TVLSI. The extended papers will undergo a separate thorough peer review, as any papers submitted to TVLSI.

Location

Aalborg, Denmark

Sponsors:



IEEE NorCAS SECRETARIAT:
Tampere University
Korkeakoulunkatu 1 D 419
FI-33014 Tampere University
Finland
Tel: +358 40 506 4460
Email: norcass@tuni.fi

events.tuni.fi/norcass2024

