Call for Papers

2023 IEEE NorCAS Conference
IEEE Nordic Circuits and Systems Conference
31 October - 1 November 2023 Aalborg, Denmark

General Scope of the Conference
The IEEE NorCAS conference is the main circuits and systems event of the Nordic countries representing both academia as well as electronics industry. The IEEE NorCAS conference is a merge of the well-established conferences NORCHIP and SoC. The annual IEEE CAS co-sponsored conference covers all fields of electronics design, spanning from complex digital systems to advanced analog and mixed-mode circuits. The wide scope of the conference promotes cross-field collaboration not only between academia but also with industry. Papers of high scientific and technical quality are presented together with selected invited speakers and pre-conference tutorial sessions.

Technical Program
The 2023 IEEE NorCAS conference will take place on 31 October - 1 November 2023. The Steering Committee invites people from academia and industry to contribute papers. Contributions for the 2023 conference in Aalborg shall be in the following areas:

**ANALOG**
- Biomedical circuits and systems
- Sensor interface circuits, data-converters
- Wireless circuits and systems
- Mixed mode designs, high frequency circuits, design for testability

**DIGITAL**
- Microelectronics/nanoelectronics
- Embedded systems, FPGA and prototyping
- Sensors, Imagers, MEMS devices
- Measurement, modeling, and simulation of hardware
- CAE, design tools, VLSI design methodology
- Machine learning, neural networks
- Packaging and interconnecting
- System-level design flow and methodology

**SoC**
- Network-on-Chip
- Hardware/software co-design and reuse
- Multicore and manycore platforms
- Reconfigurable architectures
- SoC verification and testing
- Processor and memory design for SoC
- Runtime environments, emulation and simulation

**Special Sessions**
- Approximate Computing Circuits and Systems
- Circuits and Systems in AI and Neuromorphic Computing
- Digital Design and Verification using Chisel
- Reconfigurable architectures
- SoC verification and testing
- Processor and memory design for SoC
- Runtime environments, emulation and simulation

Submission of Contributions
Papers will be selected by the Steering Committee on the basis of a full-length paper (4-6 A4 pages) based on a peer review performed by the Technical Programme Committee. There may be a 7th page with references only.

Papers may be selected for either lecture or poster presentation. Equal quality measures will be applied to posters and lectures. All accepted papers will be published in the conference proceedings and submitted for inclusion in IEEE Xplore.

Dates and Deadlines
Special session proposals: June 15, 2023
Paper submission deadline: August 31, 2023 (extended)
Notification of acceptance: September 20, 2023
Final camera-ready papers: October 4, 2023

The proceedings will be composed from camera ready pdf files supplied by authors. The paper must follow the IEEE 2-column standard as described in the paper submission guidelines in the author info section at events.tuni.fi/norcas2023. Instructions on special session proposals are found on the web page, too.

The submission site is at www.confprotool/norcas2023

Top papers will be invited to be extended and submitted to a special section planned for IEEE Transactions on Very Large Scale (VLSI) Systems - TVLSI. The extended papers will undergo a separate thorough peer review, as any papers submitted to TVLSI.

Location
Aalborg, Denmark

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