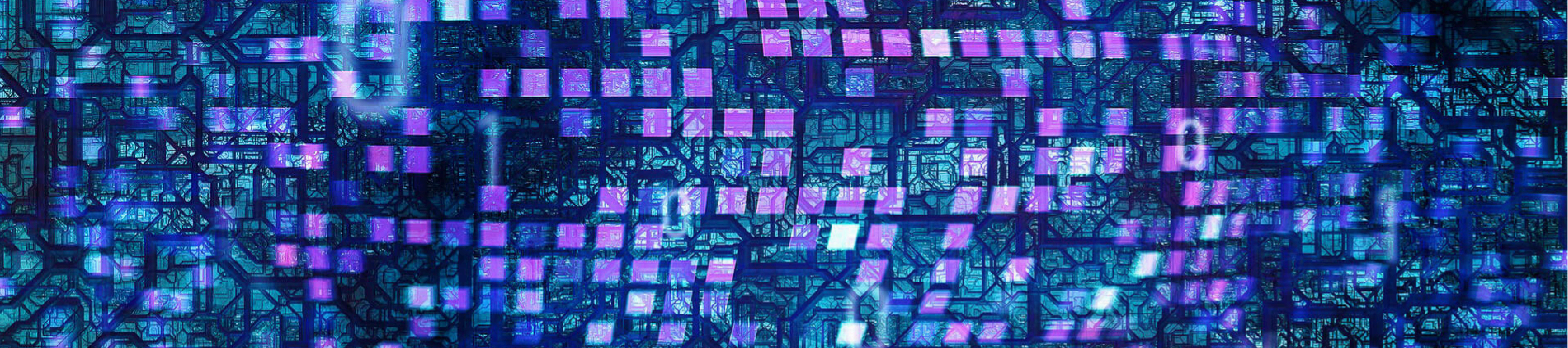




Secure SoC Design for European Growth and Resilience

Thursday 22 Oct, at 9:00-12:00 | Technopolis Yliopistonrinne, Enqvist, building A

This This SoC HUB networking event, organized as a side event of the NorCAS 2026 Conference, brings together researchers, semiconductor manufacturers, chip designers, technology innovators, and industry stakeholders to explore how secure System-on-Chip (SoC) development can strengthen growth, resilience, and cybersecurity. The event provides a unique opportunity to connect the latest research with industrial needs and emerging market opportunities in secure semiconductor technologies.

The semiconductor sector faces increasing product complexity combined with low-volume, high-mix production environments. Addressing this challenge requires stronger collaboration across the ecosystem, new innovation opportunities, and secure semiconductor solutions that can be scaled effectively across industries.

Key topics include:

- Secure SoC development and cyber resilience
- Industry requirements for secure and resilient chip solutions
- Semiconductor funding opportunities
- Research and innovation in secure semiconductor technologies
- Addressing the low-volume, high-mix manufacturing challenge

The programme will conclude with networking opportunities, providing an excellent platform for researchers, companies, and technology stakeholders to establish new collaborations, discuss research commercialization, and explore future projects.

This event is organized as part of the Power from Chips project (2025–2027), co-funded by the European Union (ERDF).

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Welcome!

REGISTER BY OCT 20

Kind regards,
Sampo Härkönen
Development Specialist
Business Tampere

